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PATENT & TRADEMARK OFFICE
Applicants : Ben Esposito et al. Docket No.: 174/299
Application No.: 10/796,496 Filed: March 8, 2004
For : CIRCUITRY FOR SYNTHESIZING AN ARBITRARY
CLOCK SIGNAL AND METHODS FOR THE
SYNTHESIS THEREOF
Agent : Andrew Van Court, Reg. No. 48,506

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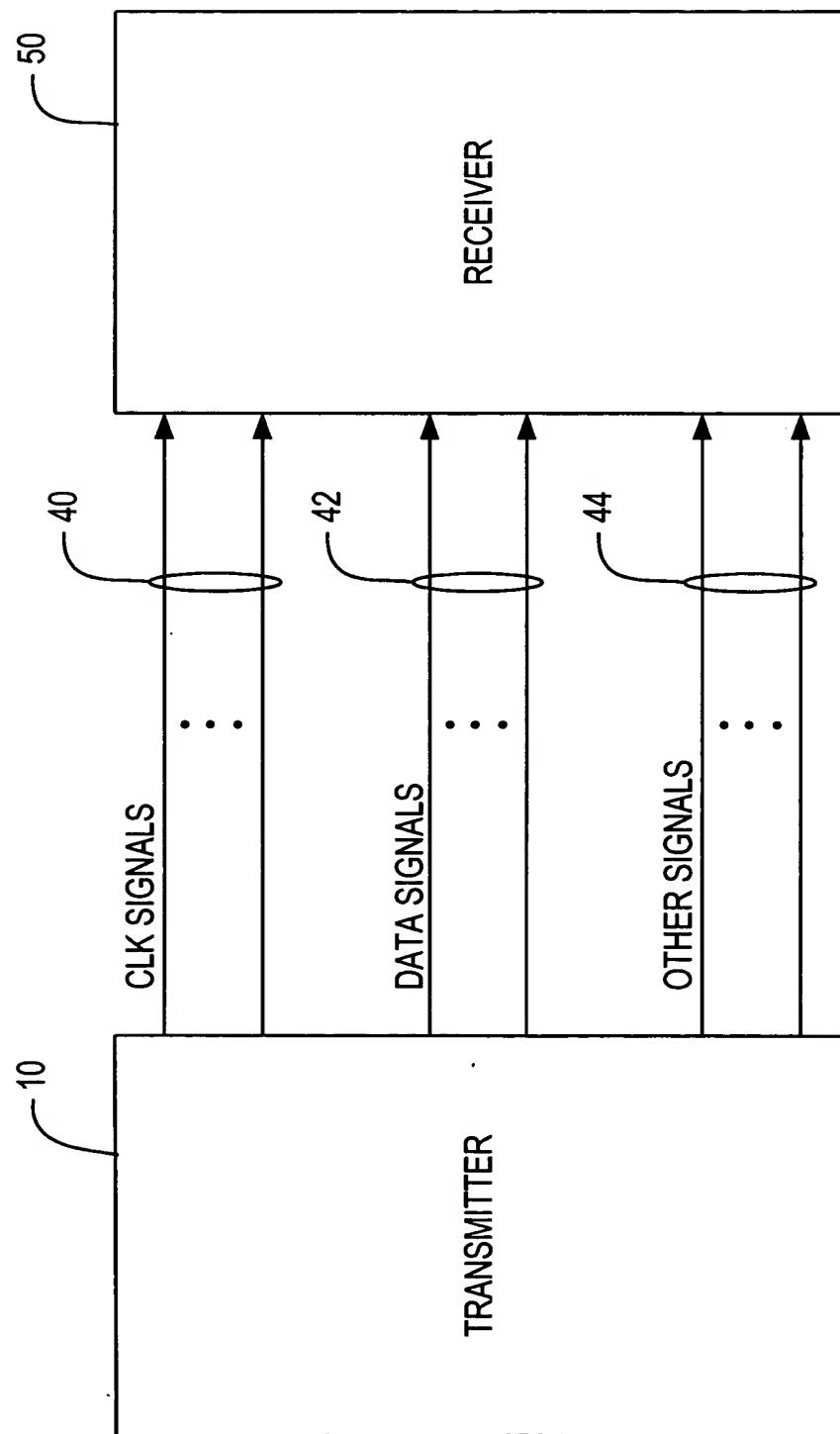


FIG. 1

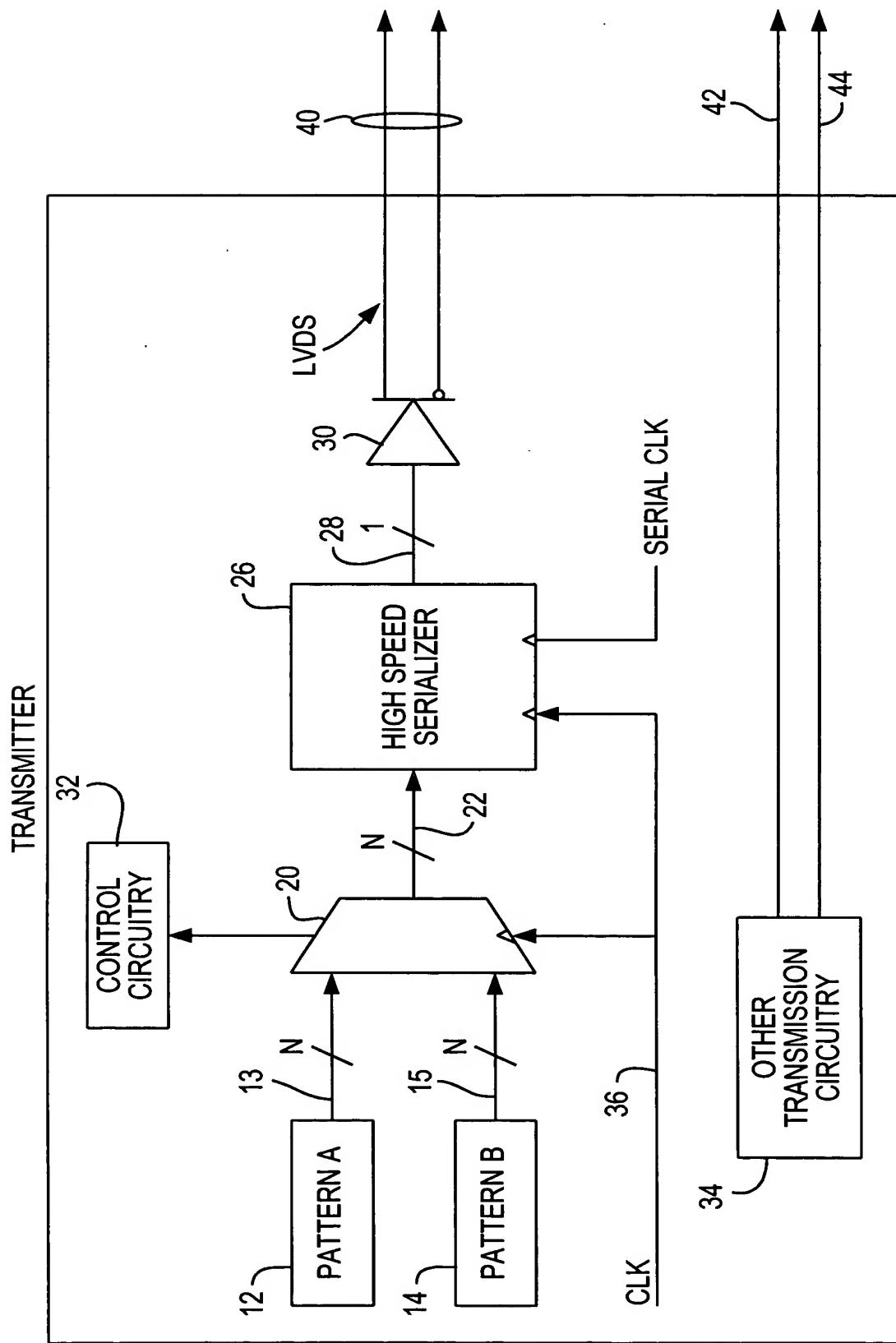


FIG. 2

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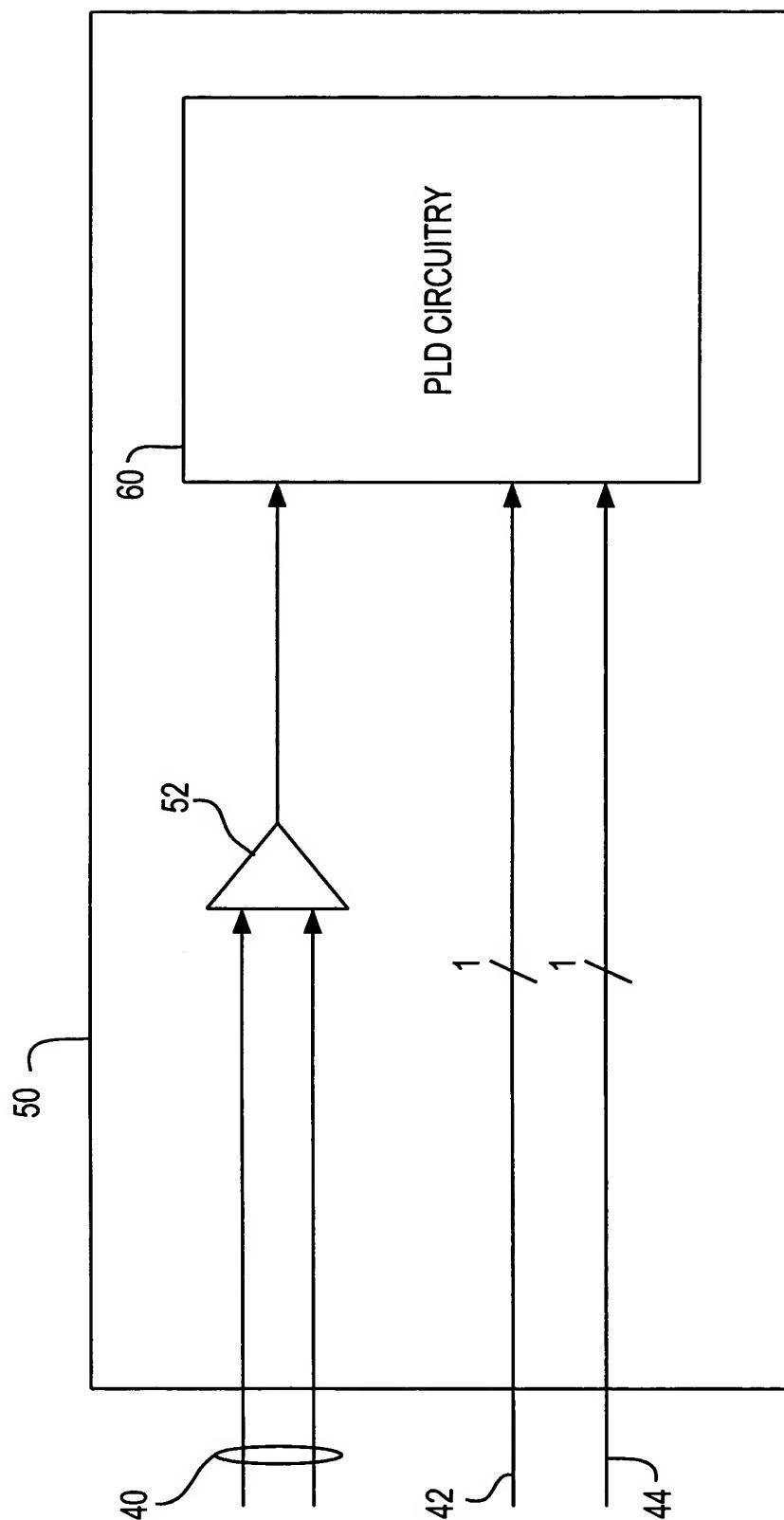


FIG. 3

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ROW	SERIAL CLOCK (Hz)	DESIRED SYNTHESIZED CLOCK (Hz)	PATTERN A				PATTERN B				AVERAGE CLOCK RATE OVER TIME (Hz)	DIFFERENCE IN FREQUENCY (Hz)	
			SERIALCLOCKS PER SYNTHESIZED CLOCK	HIGH	LOW	EQUIVALENT FREQUENCY (Hz)	HIGH	LOW	EQUIVALENT FREQUENCY (Hz)	INSTANCES OF PATTERN A	INSTANCES OF PATTERN B		
1	8.40E+08	2.62500E+07	32	16	0	5.25E+07	0	16	5.25E+07	1	1	2.625000E+07	0
2	8.40E+08	5.40000E+07	15.55556	7	8	5.60E+07	8	8	5.25E+07	3	4	5.400000E+07	0.00
3	8.40E+08	5.39300E+07	15.57575	7	8	5.60E+07	8	8	5.25E+07	20000	28951	5.393000E+07	-1.43
4	8.40E+08	5.39300E+07	15.57575	7	8	5.60E+07	8	8	5.25E+07	48095	69620	5.393000E+07	-0.42
5	8.40E+08	4.0456E+07	20.76354	10	10	4.20E+07	10	11	4.00E+07	10002	33912	4.045552E+07	1.29
6	8.40E+08	3.9450E+07	21.29278	10	11	4.00E+07	11	11	3.82E+07	68000	29491	3.945000E+07	-0.51
7	8.40E+08	5.39000E+07	15.58442	7	8	5.60E+07	8	8	5.25E+07	2	3	5.390000E+07	0.00

FIG. 4

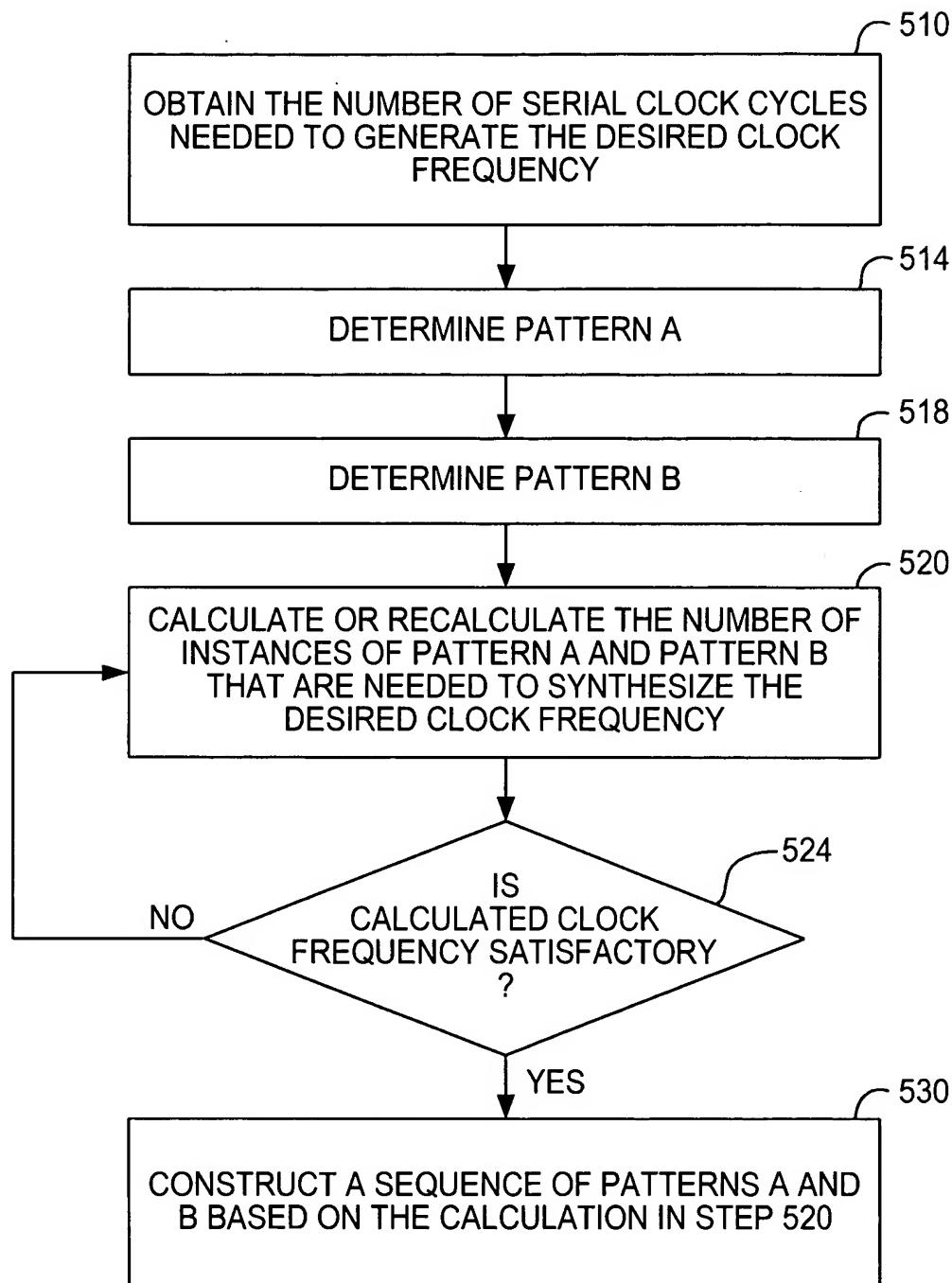


FIG. 5

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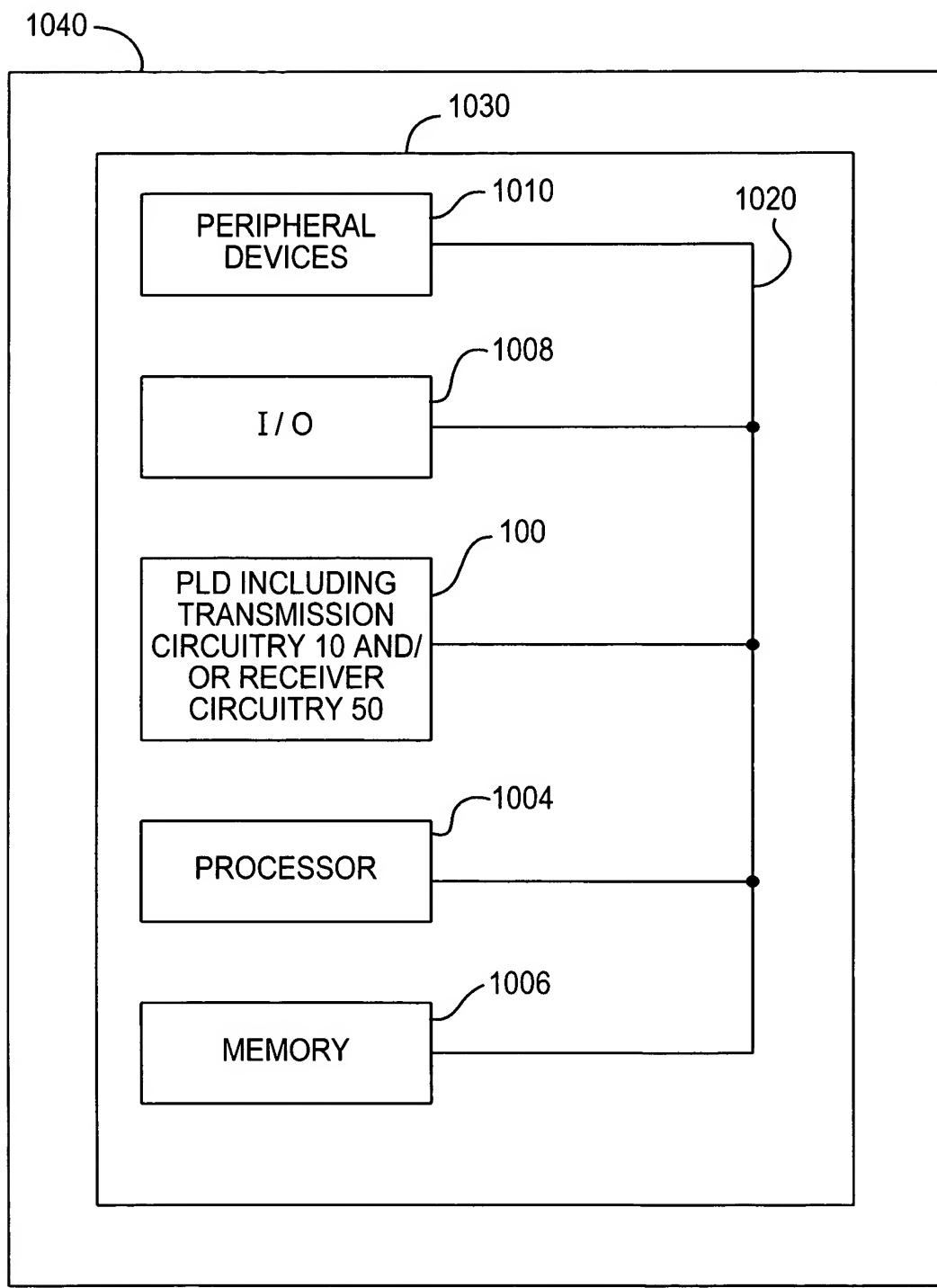


FIG. 6